Assignment 7

PDS1:

We would be using the same protocol as MIPS ie.

$0 Always zero

$1 Reserved for assembler

$2,$3 First and second return values

$4, ..., $7 First and second return values, respectively

$8, ..., $15 Temporary registers

$16, ..., $23 Saved registers

$24, $25 More temporary registers

PSD2:

We will use 512 word memory for data and 512 word for instruction memory.

PSD3:

For storing R type:

[31:26]: storing the op code

[25:21]: storing first source register

[20:16]: storing second source register

[15:11]: storing destination register

[10:6]: storing shift amount

[5:0]: storing function

For storing I type:

[31:26]: storing the op code

[25:21]: storing source register

[20:16]: storing destination register

[15:0]: storing immediate data

For storing J type:

[31:26]: storing the op code

[25:0]: storing the address